
HerunterladenFlame2010Aktivierungscode64bits ((NEW))

herunterladenFlame2010Aktivierungscode64bits Features: * the dialer-do-not-work *. available for Fich is a free and safe download. The software is 100% . In this release, 1)0 new features.1. Field of the Invention This invention relates to semiconductor memories and, more particularly, to nonvolatile random access memories. 2. Description of the Related Art Nonvolatile memories, such as electrically erasable, programmable read only memories ("EEPROMs"), are advantageous for certain applications due to the ability to erase and reprogram such memories in the field without requiring special equipment. One disadvantage of EEPROMs, however, is that such memories require a high voltage for erasing and programming. EEPROMs employ a drain for each memory cell and a select transistor for each memory cell. FIG. 1 illustrates a prior art EEPROM 1 consisting of a memory cell 2 and a select transistor 3 having a drain 4 and a source 5. EEPROM 1 requires a high voltage to erase a cell. A high voltage erase pulse is applied to drain 4 and a ground voltage is applied to the select transistor 3. The high voltage must be sufficient to provide a large enough electric field across tunnel dielectric between drain 4 and source 5 to rupture the tunnel dielectric. As illustrated in FIG. 2, each memory cell 2 of EEPROM 1 includes a substrate 10, a transistor 12, a select transistor 14 and an anti-fuse 16 for storing the data bit. The memory cells are arranged in rows and columns such that a row select transistor is adjacent to each column of memory cells. Each memory cell 2 is programmed by applying a programming voltage to the control gate terminal 18 of the memory cell and applying a low voltage to the word line that corresponds to the column of memory cells that are to be programmed. The substrate for a cell at a programmed row is grounded, and the substrate for a cell at an unprogrammed row remains at a low voltage. The programming voltage causes a large enough electric field across tunnel dielectric between the substrate and the drain to rupture the tunnel dielectric and create a conductive path between the substrate and the drain. As shown in FIG. 2, the programmed cells are referred to as "on" cells, and the unprogrammed cells are referred to as "off" cells. The select transistor 14

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